



of a number of well-known techniques such as Gauss-Jordan elimination techniques. The selected equations correspond to the deterministically specified bits:

In the claims:

 $\mathcal{E}\mathsf{A}$

13. (Amended) The method of claim 1 wherein decompressing the compressed test pattern comprises generating one or more bits of the decompressed pattern by logically combining two or more bits of the compressed test pattern.

Respectfully submitted,

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